

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

Patent No.:

Filed:

Issue Date:

Title:

Commissioner for Patents
Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)

As assignee of record of each of the patents listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patents are hereby revoked, and

NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this patent/application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this patent/application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number:

15650

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

Scott A. Horstemeyer, Reg. No. 34,183
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, L.L.P.
400 Interstate North Parkway
Atlanta, Georgia 30339
770-933-9500

ASSIGNEE OF ENTIRE INTEREST

BROADCOM CORPORATION

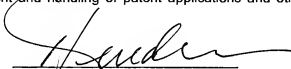
5300 California Avenue
Irvine, California 92617-3038

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Broadcom Corporation, I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: _____

9/12/11



Dee Henderson
Director - Intellectual Property Administration
Broadcom Corporation

Attachment A

| No. | Patent No | BD No. / TKHR No. | Patent Title | Issue Date | BD Assign't (Reel/Frame) |
|-----|-----------|-----------------------------|---|------------|--------------------------|
| 1 | 7356051 | BU1803.1 050228-5650 | DIGITAL VISUAL INTERFACE WITH AUDIO AND AUXILIARY DATA | 04/08/08 | 012917/0176 |
| 2 | 7161998 | BU1803.2 050228-5660 | DIGITAL PHASE LOCKED LOOP FOR REGENERATING THE CLOCK OF AN EMBEDDED SIGNAL | 01/09/07 | 012542/0390 |
| 3 | 7636409 | BU1803.2C 050228-5670 | DIGITAL PHASE LOCKED LOOP FOR REGENERATING THE CLOCK OF AN EMBEDDED SIGNAL | 12/22/09 | 012542/0390 |
| 4 | 8024389 | BU2017 050228-5680 | INVERSE DISCRETE COSINE TRANSFORM SUPPORTING MULTIPLE DECODING PROCESSES | 09/20/11 | 012761/0426 |
| 5 | 6501480 | BU1267 050228-5690 | GRAPHICS ACCELERATOR | 12/31/02 | 011368/0527 |
| 6 | 7007031 | BU2007.3 050228-5700 | MEMORY SYSTEM FOR VIDEO DECODING SYSTEM | 02/28/06 | 012761/0414 |
| 7 | 7302503 | BU2007.1 050228-5710 | MEMORY ACCESS ENGINE HAVING MULTI-LEVEL COMMAND STRUCTURE | 11/27/07 | 014295/0066 |
| 8 | 7095341 | BU2006.1C 050228-5720 | PROGRAMMABLE VARIABLE-LENGTH DECODER | 08/22/06 | 013675/0529 |
| 9 | 6178198 | BU1008 050228-5730 | APPARATUS FOR, AND METHOD OF, PROCESSING SIGNALS TRANSMITTED OVER A LOCAL AREA NETWORK | 01/23/01 | 011066/0118 |
| 10 | 6459730 | BU1008C 050228-5740 | APPARATUS FOR, AND METHOD OF, PROCESSING SIGNALS TRANSMITTED OVER A LOCAL AREA NETWORK | 10/01/02 | 011066/0118 |
| 11 | 7321619 | BU1008C2 050228-5750 | APPARATUS FOR, AND METHOD OF, PROCESSING SIGNALS TRANSMITTED OVER A LOCAL AREA NETWORK | 01/22/08 | 011066/0118 |
| 12 | 7778313 | BU1248C2 050228-5760 | PHY CONTROL MODULE FOR A MULTI-PAIR GIGABIT TRANSCEIVER | 08/17/10 | 010763/0692 |
| 13 | 7733952 | BU1247C2D4C1 050228-5770 | MULTI-PAIR GIGABIT ETHERNET TRANSCEIVER HAVING ADAPTIVE DISABLING OF CIRCUIT ELEMENTS | 06/08/10 | 010694/0029 |
| 14 | 6563333 | BU1249C 050228-5780 | DYNAMIC REGISTER WITH IDDQ TESTING CAPABILITY | 05/13/03 | 010393/0131 |
| 15 | 6737859 | BU1249C2 050228-5790 | DYNAMIC REGISTER WITH IDDQ TESTING CAPABILITY | 05/18/04 | 010393/0131 |
| 16 | 6927565 | BU1249C3 050228-5800 | DYNAMIC REGISTER WITH IDDQ TESTING CAPABILITY | 08/09/05 | 010393/0131 |
| 17 | 7129697 | BU1249C4 050228-5810 | DYNAMIC REGISTER WITH IDDQ TESTING CAPABILITY | 10/31/06 | 010393/0131 |
| 18 | 6389077 | BU1256C 050228-5820 | Adaptively configurable class-A/class-B transmit DAC for transceiver emission and power consumption control | 05/14/02 | 010536/0625 |
| 19 | 6594304 | BU1256C2 050228-5830 | ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER | 07/15/03 | 010536/0625 |

| No. | Patent No | BD No. / TKHR No. | Patent Title | Issue Date | BD Assign't (Reel/Frame) |
|-----|-----------|---------------------------|--|------------|-----------------------------|
| | | | CONSUMPTION CONTROL | | |
| 20 | 7630434 | BU1292C 050228-5840 | HIGH-SPEED DECODER FOR A MULTI-PAIR GIGABIT TRANSCEIVER | 12/08/09 | 010163/0370 |
| 21 | 6249544 | BU1295 050228-5850 | SYSTEM AND METHOD FOR HIGH-SPEED DECODING AND ISI COMPENSATION IN A MULTI-PAIR TRANSCEIVER SYSTEM | 06/19/01 | 010163/0561 |
| 22 | 6201796 | BU1020 050228-5860 | STARTUP PROTOCOL FOR HIGH THROUGHPUT COMMUNICATIONS SYSTEMS | 03/13/01 | 009173/0984 |
| 23 | 6819709 | BU1020C 050228-5870 | STARTUP PROTOCOL FOR HIGH THROUGHPUT COMMUNICATIONS SYSTEM | 11/16/04 | 009173/0984 |
| 24 | 7386038 | BU1020C2 050228-5880 | STARTUP PROTOCOL FOR HIGH THROUGHPUT COMMUNICATIONS SYSTEMS | 06/10/08 | 009173/0984 |
| 25 | 6449271 | BU1012C 050228-5890 | SYSTEM FOR AND METHOD OF, PROVIDING A HEADER AND A TRAILER IN DATA PACKETS | 09/10/02 | 009092/0113 |
| 26 | 6909712 | BU1012C2 050228-5900 | SYSTEM FOR AND METHOD OF PROVIDING A HEADER AND A TRAILER IN DATA PACKETS | 06/21/05 | 009092/0113 |
| 27 | 7440520 | BU1573C 050228-5910 | SYSTEM AND METHOD FOR TERRESTRIAL HIGH-DEFINITION TELEVISION RECEPTION | 10/21/08 | 013015/0832 |
| 28 | 7263275 | BU2225.2 050228-5920 | SYSTEM AND METHOD OF MANIPULATING A SYSTEM TIME CLOCK IN AN AUDIO/VIDEO DECODING SYSTEM | 08/28/07 | 013185/0757 |
| 29 | 7595808 | BU2253C 050228-5930 | METHOD AND APPARATUS FOR UPDATING A COLOR LOOK-UP TABLE | 09/29/09 | 013600/0379 |
| 30 | 7006152 | BU2225.3 050228-5940 | SYSTEM AND METHOD FOR PROVIDING PICTURE-IN-PICTURE TIMEBASE MANAGEMENT | 02/28/06 | 013185/0763 |
| 31 | 6982727 | BU2314 050228-5950 | SYSTEM AND METHOD FOR PROVIDING GRAPHICS USING GRAPHICAL ENGINE | 01/03/06 | 013331/0625 |
| 32 | 7151894 | BU2531 050228-5960 | BIT ERROR RATE BASED SYSTEM AND METHOD FOR OPTIMIZING COMMUNICATION SYSTEM PERFORMANCE | 12/19/06 | 014248/0767 |
| 33 | 7548692 | BU2531C 050228-5970 | BIT ERROR RATE BASED SYSTEM AND METHOD FOR OPTIMIZING COMMUNICATION SYSTEM PERFORMANCE | 06/16/09 | 014248/0767 |
| 34 | 7840134 | BU2531C2 050228-6090 | BIT ERROR RATE BASED SYSTEM AND METHOD FOR OPTIMIZING COMMUNICATION SYSTEM PERFORMANCE | 11/23/10 | 014248/0767 |
| 35 | 7206366 | BU2533 050228-6100 | SYSTEM AND METHOD FOR PROGRAMMABLY ADJUSTING GAIN AND FREQUENCY RESPONSE IN A 10-GIGABIT ETHERNET/FIBRE CHANNEL SYSTEM | 04/17/07 | 014017/0722 |
| 36 | 7508837 | BU2645 050228-6110 | SYSTEM AND METHOD FOR RECEIVE QUEUE PROVISIONING | 03/24/09 | 014380/0608 |
| 37 | 7142013 | BU2658.1C2 050228-6120 | ONE-LEVEL ZERO-CURRENT-STATE EXCLUSIVE OR (XOR) GATE | 11/28/06 | 013991/0709 |
| 38 | 7496647 | BU2800 050228-6130 | PERSONAL INTER-HOME MEDIA EXCHANGE NETWORK | 02/24/09 | 014222/0265 |
| 39 | 7773511 | BU22775D1 | GENERIC ON-CHIP HOMING AND | 08/10/01 | 014060/0087 |

| No. | Patent No | BD No. / TKHR No. | Patent Title | Issue Date | BD Assign't (Reel/Frame) |
|-----|-----------|----------------------------|---|------------|----------------------------|
| | | 050228-6270 | RESIDENT, REAL-TIME BIT EXACT TESTS | | |
| 40 | 7595842 | BU2860 050228-6140 | SYSTEM AND METHOD FOR THREE DIMENSIONAL COMB FILTERING | 09/29/09 | 014716/0211 |
| 41 | 7595843 | BU2721 050228-6150 | PULLDOWN FIELD DETECTOR | 09/29/09 | 015077/0774 |
| 42 | 6363129 | BU1225 050228-6160 | TIMING RECOVERY SYSTEM FOR A MULTI-PAIR GIGABIT TRANSCEIVER | 03/26/02 | 010393/0134 |
| 43 | 7092468 | BU1225CD 050228-6170 | TIMING RECOVERY SYSTEM FOR A MULTI-PAIR GIGABIT TRANSCEIVER | 08/15/06 | 010393/0134 |
| 44 | 6968167 | BU1545.11C 050228-6180 | ADAPTIVE RADIO TRANSCEIVER WITH CALIBRATION | 11/22/05 | 011345/0484 |
| 45 | 7139540 | BU1545.11C 050228-6190 | ADAPTIVE RADIO TRANSCEIVER WITH CALIBRATION | 11/21/06 | 011345/0484 |
| 46 | 7555263 | BU1545 050228-6200 | ADAPTIVE RADIO TRANSCEIVER | 06/30/09 | 011345/0484 |
| 47 | 7299006 | BU1545I 050228-6210 | ADAPTIVE RADIO TRANSCEIVER | 11/20/07 | 011831/0398 |
| 48 | 7548726 | BU1545I2 050228-6220 | ADAPTIVE RADIO TRANSCEIVER WITH A BANDPASS FILTER | 06/16/09 | 011823/0791 |
| 49 | 7558556 | BU1545I4 050228-6230 | ADAPTIVE RADIO TRANSCEIVER WITH SUBSAMPLING MIXERS | 07/07/09 | 011595/0804 |
| 50 | 6961546 | BU1545I4 050228-6240 | ADAPTIVE RADIO TRANSCEIVER WITH OFFSET PLL WITH SUBSAMPLING MIXERS | 11/01/05 | 011581/0985 |
| 51 | 7082293 | BU1545I5 050228-6250 | ADAPTIVE RADIO TRANSCEIVER WITH CMOS OFFSET PLL | 07/25/06 | 011785/0092 |
| 52 | 7463868 | BU1545I4C 050228-6260 | ADAPTIVE RADIO TRANSCEIVER WITH OFFSET PLL WITH SUBSAMPLING MIXERS | 12/09/08 | 011581/0985 |
| 53 | 7512378 | BU1545IC 050228-6280 | ADAPTIVE RADIO TRANSCEIVER | 03/31/09 | 011831/0398 |
| 54 | 7783251 | BU1545IC2 050228-6290 | ADAPTIVE RADIO TRANSCEIVER | 08/24/10 | 011831/0398 |
| 55 | 7904037 | BU1545I.4C2 050228-6300 | ADAPTIVE RADIO TRANSCEIVER WITH OFFSET PLL WITH SUBSAMPLING MIXERS | 03/08/11 | 011581/0985 |
| 56 | 7880809 | BU3455C1 050228-6310 | METHOD AND SYSTEM FOR MOTION ADAPTIVE DEINTERLACER WITH INTEGRATED DIRECTIONAL FILTER | 02/01/11 | 015457/0972 |
| 57 | 7372839 | BU3493 050228-6320 | GLOBAL POSITIONING SYSTEM (GPS) BASED SECURE ACCESS | 05/13/08 | 014747/0704 |
| 58 | 7305307 | BU2860.4 050228-6330 | PHASE BASED SYSTEM AND METHOD FOR DETERMINING SIGNAL CONSISTENCY | 12/04/07 | 019787/0302 |
| 59 | 7106820 | BU4726 050228-6340 | SYSTEM AND METHOD FOR ESTABLISHING WORD SYNCHRONIZATION | 09/12/06 | 017262/0789 |
| 60 | 7490284 | BU3869 050228-6350 | META-VITERBI ALGORITHM FOR USE IN COMMUNICATION SYSTEMS | 02/10/09 | 016250/0579 016363/0941 |